Fig.1

EGRESS E Fig.2 ပ В ⋖

²/₃₃

Fig.3

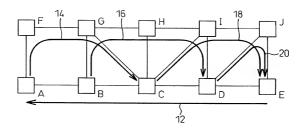


Fig.4

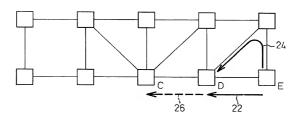


Fig.5

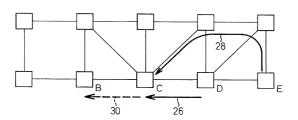


Fig.6

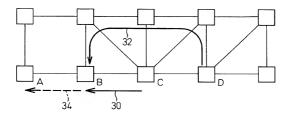


Fig.7

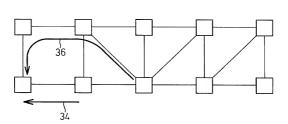


Fig.8

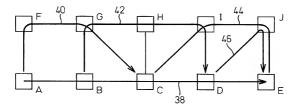
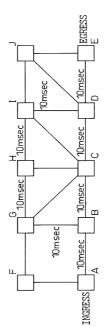


Fig.9



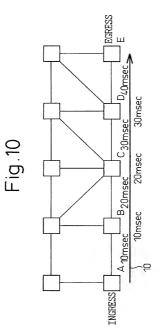
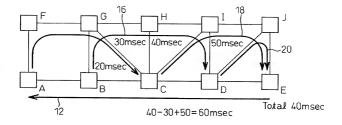


Fig.11



EGRESS 150 Fig.12 ပ ф INGRESS

Fig.13

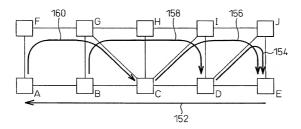


Fig.14

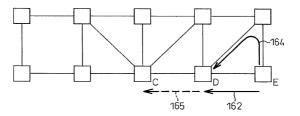


Fig.15

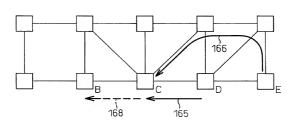


Fig.16

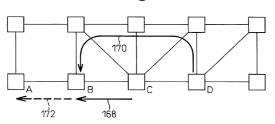


Fig.17

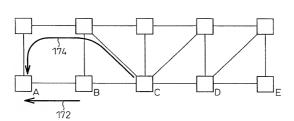
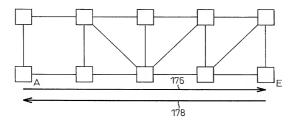
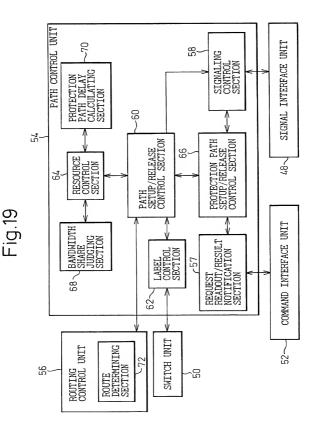


Fig.18



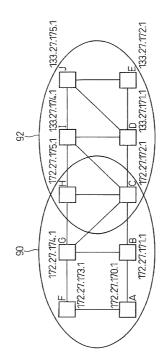


85 87 8

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Fig.20

Fig.21



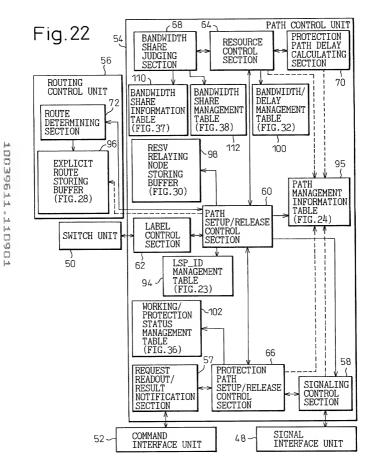
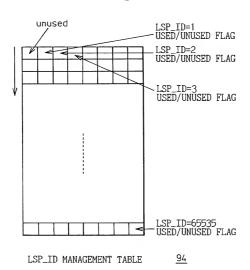


Fig.23



¹⁸⁄₃₃ Fig.24

| PATH ID | |
|---------|---|
| / | |
| ~ | EGRESS OR PML NODE ID |
| | REQUIRED BANDWIDTH [Mbit/s] |
| | REQUIRED DELAY [ms] |
| | DELAY FROM INGRESS [ms] |
| | WORKING/PROTECTION IDENTIFICATION |
| | PROTECTION NEEDED/NOT-NEEDED |
| | PATH STATE |
| | OUTPUT PORT NUMBER |
| | INPUT PORT NUMBER |
| | EXPLICIT ROUTE STORING BUFFER ADDRESS |
| | END-TO-END DELAY |
| | RESV RELAYING NODE STORING BUFFER ADDRESS |
| | CORRESPONDING PATH ID |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| | |

PATH MANAGEMENT INFORMATION TABLE

INFORMATION SETUP IN PATH MANAGEMENT INFORMATION TABLE (WORKING PATH AT INGRESS NODE)

| THEORIGINAL SELOT IN TAIL HANDERS THE OWNER OF TABLE (NOUVERS LAID AT THORSES RODE) | A CHAINIAN I ADDLE (MOINIANG I | THE ALL LINGINGS MODE) |
|---|--|------------------------|
| FIELD NAME | SET VALUE | REMARKS |
| EGRESS OR PML NODE ID | 133.27.172.1 | |
| REQUIRED BANDWIDTH [Mbit/s] | 10 | |
| REQUIRED DELAY [ms] | 100 | |
| DELAY FROM INGRESS [ms] | 0 | |
| WORKING/PROTECTION IDENTIFICATION | O: WORKING | SET UP DURING PATH |
| PROTECTION NEEDED/NOT-NEEDED | 1: PROTECTION NEEDED | MESSAGE PROCESSING |
| PATH STATE | PATH STATE: 1: PATH BEING SET UP 2: PATH BEING RELEASED 3: COMMUNICATING | |
| OUTPUT PORT NUMBER | e | |
| INPUT PORT NUMBER | O (NONE) | |
| EXPLICIT ROUTE STORING BUFFER ADDRESS | MEMORY ADDRESS | |
| END-TO-END DELAY | | SET UP DURING RESV |
| RESV RELAYING NODE STORING BUFFER ADDRESS | ESS | MESSAGE INCOESSING |
| CORRESPONDING PATH ID | | |

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INFORMATION SETUP IN PATH MANAGEMENT INFORMATION TABLE (WORKING PATH AT NODE B)

| FIELD NAME | SET VALUE | REMARKS |
|---|--|--------------------|
| EGRESS OR PML NODE ID | 133.27.172.1 | |
| REQUIRED BANDWIDTH [Mbit/s] | 10 | |
| REQUIRED DELAY [ms] | 100 | |
| DELAY FROM INGRESS [ms] | 10 | |
| WORKING/PROTECTION IDENTIFICATION | O: WORKING | SET UP DURING PATH |
| PROTECTION NEEDED/NOT-NEEDED | 1: PROTECTION NEEDED | MESSAGE PROCESSING |
| PATH STATE | PATH STATE: 1: PATH BEING SET UP 2: PATH BEING RELEASED 3: COMMUNICATING | |
| OUTPUT PORT NUMBER | 1 | |
| INPUT PORT NUMBER | 2 | |
| EXPLICIT ROUTE STORING BUFFER ADDRESS | MEMORY ADDRESS | |
| END-TO-END DELAY | 07 | SET UP DURING RESV |
| RESV RELAYING NODE STORING BUFFER ADDRESS | MEMORY ADDRESS | MESSAGE FROCESSING |
| CORRESPONDING PATH ID | NODE B (172.27.171)+ LSP_ID (2) | |

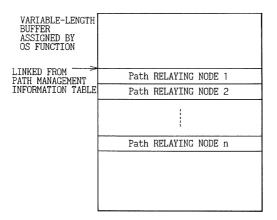
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INFORMATION SETUP IN PATH MANAGEMENT INFORMATION TABLE (PROTECTION PATH AT NODE B)

| INFORMATION SELOF IN FAIR MANAGEMENT INFORMATION TABLE (FROIDSLING FAIR AT NODE B) | DEFINITION LABLE (FRUIECLIUM | FAIR AI NODE D) |
|--|---|--------------------|
| FIELD NAME | SET VALUE | REMARKS |
| EGRESS OR PML NODE ID | 133.27.171.1 | |
| REQUIRED BANDWIDTH [Mbit/s] | 10 | |
| REQUIRED DELAY [ms] | 100 | |
| DELAY FROM INGRESS [ms] | 10 | |
| WORKING/PROTECTION IDENTIFICATION | 1: PROTECTION | SET UP DURING PATH |
| PROTECTION NEEDED/NOT-NEEDED | O: PROTECTION NOT NEEDED | MESSAGE PROCESSING |
| PATH STATE | PATH STATE: 1: PATH BEING SET UP 2: PATH BEING RELEASED 3: COMMUNICATING | |
| OUTPUT PORT NUMBER | 2 | |
| INPUT PORT NUMBER | 2 | |
| EXPLICIT ROUTE STORING BUFFER ADDRESS | MEMORY ADDRESS | |
| END-TO-END DELAY | 09 | SET UP DURING RESV |
| RESV RELAYING NODE STORING BUFFER ADDRESS | MEMORY ADDRESS | DUTCOTONI TOUCOTI |
| CORRESPONDING PATH ID | NODE A (172.27.170)+ LSP_ID (1) | |

²¹/₃₃

Fig.28



EXPLICIT ROUTE STORING BUFFER 96

Fig.29

INFORMATION SETUP IN EXPLICIT ROUTE STORING BUFFER (WORKING PATH AT INGRESS NODE)

| FIELD NAME | SET VALUE |
|----------------------|-----------------------|
| Path RELAYING NODE 1 | 172.27.171.1 (NODE B) |
| Path RELAYING NODE 2 | 172.27.172.1 (NODE C) |

| VARIABLE-LENGTH BUFFER ASSIGNED BY OS FUNCTION | | |
|---|--------------------------------------|-----------|
| LINKED FROM PATH MANAGEMENT | Resv RELAYING NODE 1 | |
| INFORMATION TABLE | Resv RELAYING NODE 2 | |
| TABLE | | |
| | Resv RELAYING NODE n | |
| | | |
| | RESV RELAYING NODE STORING BUFFER | <u>98</u> |

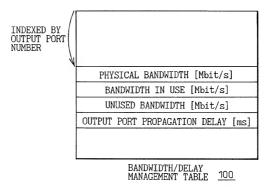
Fig.31

INFORMATION SETUP IN RESY RELAVING NODE STORING BUFFER (NORKING PATH AT NODE B)

| SET VALUE | 172.27.172.1 (NODE C) | 133.27.171.1 (NODE D) | 133.27.172.1 (NODE E) | |
|------------|-----------------------|-----------------------|-----------------------|--|
| FIELD NAME | Resv RELAYING NODE 1 | Resv RELAYING NODE 2 | Resv RELAYING NODE 3 | |

²⁵/₃₃

Fig.32



INFORMATION SETUP IN BANDMIDTH/DELAY MANAGEMENT TABLE (OUTPUT PORT 3 OF INGRESS NODE)

| SET VALUE | 10240 | 5000 | 2240 | 10 | |
|------------|-----------------------------|---------------------------|---------------------------|------------------------------------|--|
| FIELD NAME | PHYSICAL BANDWIDTH [Mbit/s] | BANDWIDTH IN USE [Mbit/s] | UNUSED BANDWIDTH [Mbit/s] | OUTPUT PORT PROPAGATION DELAY [ms] | |

²⁷⁄33

Fig.34

INFORMATION SETUP IN BANDMIDTH/DELAY MANAGEMENT TABLE (OUTPUT PORT 1 OF NODE B)

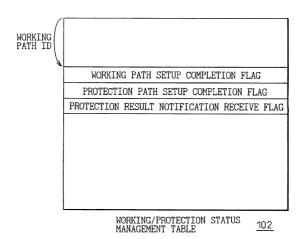
| SET VALUE | 10240 | 1000 | 9240 | 10 |
|------------|-----------------------------|---------------------------|---------------------------|------------------------------------|
| FIELD NAME | PHYSICAL BANDWIDTH [Mbit/s] | BANDWIDTH IN USE [Mbit/s] | UNUSED BANDWIDTH [Mbit/s] | OUTPUT PORT PROPAGATION DELAY [ms] |

Fig.35

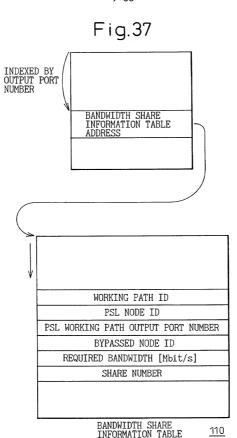
INFORMATION SETUP IN BANDMIDTH/DELAY MANAGEMENT TABLE (OUTPUT PORT 2 OF NODE B)

| SET VALUE | 10240 | 2000 | 8240 | 10 |
|------------|-----------------------------|---------------------------|---------------------------|------------------------------------|
| FIELD NAME | PHYSICAL BANDWIDTH [Mbit/s] | BANDWIDTH IN USE [Mbit/s] | UNUSED BANDWIDTH [Mbit/s] | OUTPUT PORT PROPAGATION DELAY [ms] |

Fig.36



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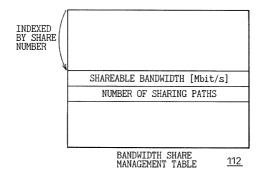


Fig.39

